GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-III (OLD) EXAMINATION - WINTER 2017

Subject Code:130902			Date:14/11/2017	
Tiı	me: 1 tructio	Attempt all questions. Make suitable assumptions wherever necessary.	70	
Q.1	(a) (b)	List and discuss all ideal characteristics of an Op-Amp. Explain with the help of circuit diagram the operation of an Op-Amp inverting amplifier. Derive an expression for the voltage gain of this amplifier.	07 07	
Q.2	(a) (b)	Write a short note on Integrator. Explain the working of IC 555 as a bistable multivibrator. OR	07 07	
	(b)	Explain the operation of dual slope ADC and state it's advantages.	07	
Q.3	(a)	Draw the typical connection diagram of 78xx regulator series and explain the function of each and every component in the circuit.	07	
	(b)	(i) Explain the De-Morgan's theorem.(ii)Write a note on Duality theorem.	07	
Q.3	(a) (b)	OR (i) Reduce the expression using Boolean laws $A\overline{B}C + B + B\overline{D} + AB\overline{D} + \overline{A}C$ (ii) Add -75 to +26 using the 8 bit 2's compliment arithmetic. (iii) convert 5C7 ₁₆ to decimal. Reduce using K mapping the expression $\sum m$ (0,1,2,3,5,7,8,9,10,12,13) and	07	
	(D)	implement it in universal logic.	U I	
Q.4	(a)	Explain priority encoder with help of truth table, K-map and AOI implementation.	07	
	(b)	Compare between TTL and CMOS logic. OR	07	
Q.4		Explain with diagram and truth table the operation of 4:1 Multiplexer. Derive full adder with the help of necessary truth table, K-map. Also express in AOI logic diagram.	07 07	
Q.5	(a) (b)	Write a short note on parallel in serial out shift register. Explain the working of Master Slave J-K flip flop. OR	07 07	
Q.5	(a) (b)	Explain with the truth table the working of R-S flip flop. What is the difference between synchronous counter and asynchronous counter?	07 07	
