Seat No.: _____ Enrolment No.____

GUJARAT TECHNOLOGICAL UNIVERSITY BE - SEMESTER- III EXAMINATION - SUMMER 2015

Subject code: 130704 Date: 27/05/26 Subject Name: COMPUTER ORGANIZATION AND ARCHITECTURE Fime: 2:30 Pm-5:00 Pm Total Marks: Instructions:				
Instr	1. 2.	Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.		
Q.1	(a) (b)	1	07 07	
Q.2	(a)	· · · · · · · · · · · · · · · · · · ·	07	
	(b)	instructions with four instructions in each type. Explain the Common Bus System with its diagram. OR	07	
	(b)		07	
Q.3	(a)	Draw the block diagram of control unit of basic computer. Explain in detail with control timing diagrams.	07	
	(b)	What is addressing mode? List and explain all addressing modes used in basic computer.	07	
Q.3	(a)	OR What is stack? Give the organization of register stack with all necessary	07	
Q.S	(4)	elements and explain the different operations of stack.	07	
	(b)	What are the main types of Interrupts? Explain each type in details.	07	
Q.4	(a)	Give the comparisons between RISC and CISC.	07	
	(b)	What is subroutine? Explain subroutine parameters and data linkage. OR	07	
Q.4	(a)	What is assembler? Explain the different phases of assembler.	07	
	(b)	Explain address sequencing with block diagram.	07	
Q.5	(a)	What is Instruction Pipeline? Explain the Four-Segment Instruction Pipeline with the suitable diagram.	07	
	(b)		07	
Q.5	(a)		07	

(b) Explain the Booth Multiplication Algorithm in detail.

instruction in detail.

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