

Seat No.: \_\_\_\_\_

Enrolment No. \_\_\_\_\_

# GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER– V(OLD) EXAMINATION – SUMMER 2019

**Subject Code:150701**

**Date:19/06/2019**

**Subject Name:Advance Processors**

**Time:02:30 PM TO 05:00 PM**

**Total Marks: 70**

**Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

- Q.1 (a)** What is segmentation? Advantages of it. List out four segments of 8086 with its purpose. **07**
- (b)** Draw and explain architecture of 8086. **07**
- Q.2 (a)** What is the difference between minimum and maximum mode of 8086? **07**
- (b)** Write 8086 program to divide 16-bit number by 8-bit number. **07**
- OR**
- (b)** Write 8086 program to transfer memory block from one location to another using string instructions. **07**
- Q.3 (a)** Explain memory bank in details. **07**
- (b)** What is interrupt? Explain IVT in detail. **07**
- OR**
- Q.3 (a)** What is the difference between instructions and directives? Explain following directives : ASSUME, PTR **07**
- (b)** Explain parameter passing methods in 8086. **07**
- Q.4 (a)** Draw and explain the block diagram of 80286. **07**
- (b)** Explain paging in details. **07**
- OR**
- Q.4 (a)** What is descriptor? Explain general format of descriptor. **07**
- (b)** Explain flag register and machine status word of 80286. **07**
- Q.5 (a)** What are the major advantages of 80486 over 80286? Draw block diagram of 80486. **07**
- (b)** Explain near and far procedure of 8086. **07**
- OR**
- Q.5 (a)** Write short note on MMX and hyper threading. **07**
- (b)** Difference between RISC and CISC architecture. **07**

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