GUJARAT TECHNOLOGICAL UNIVERSITY

	BE - SEMESTER-V (OLD) EXAMINATION – WINTER	2018	
Subject Code:150701 Date: 30/11			8
Subje	ect Name: Advance Processors		
_		al Marks:	70
Instruc			
3	 Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks. 		
Q.1	(a) Draw and explain 8086 Architecture block diagram.(b) Explain the addressing modes of 8086 with example.		07 07
Q.2	(a) Explain following 8086 instructions.1. MUL 2. LOOP 3. MOVSB 4. ADD		07
	(b)Write an assembly language program to find the factorial of t	he given no.	07
	OR		
	(b) Write an assembly language program to move data from to another.	one location	07
Q.3	(a) Define Segmentation. Advantages of Segmentation. List out fo of 8086 with its purpose.	our segments	07
	(b) Discuss Minimum mode operation of 8086.		07
Q.3	(a) Explain the parameter passing methods with example.(b) Describe Reentrant and Recursive procedures		07 07
Q.4	(a) Write a brief note on Virtual 8086 Mode		07
	(b) . Explain Pentium Processor Architecture OR		07
Q.4	(a) Explain MMX and hyper threading		07
	(b) Describe the Access Right Byte in 80286 processor.		07
Q.5	(a) Explain the concept of memory banks in 8086.(b) Explain the merits and demerits of RISC and CISC CPU.		07 07
Q.5	(a) Describe the architecture of SUN SPARC processor.(b) Describe the architectural features of Core-2 duo processor.		07 07
