GUJARAT TECHNOLOGICAL UNIVERSITY

BE – SEMESTER – VI (NEW).EXAMINATION – WINTER 2016

S	ubje	ect Code: 2160909 Date: 26/10/2016	
1	ime	ect Name: Advance Microcontrollers : 02:30 PM to 05:00 PM Total Marks: 70 etions:	
	iisti uc	 Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks. 	
Q.1	(a) (b)	Explain SPI communication protocol for 8051 microcontroller. Discuss the capture mode of PCA time in 89C51rd2 microcontroller.	07 07
Q.2	(a) (b)	Discuss the I ² C communication protocol of 89C51rd2 microcontroller. Explain features of STM32F4XXDSC.	07 07
	(b)	OR Write a 'c' program for generating a square wave with 55% duty cycle using P89V51RD2 with crystal frequency of 11.0592 MHz using PCA Timer in PWM mode of operation. What is the PWM frequency if PCA Timer is used in 12 clock mode in mode 1?	07
Q.3	(a) (b)	List out steps required to enable interrupts safely in an IRQ handler. Draw and explain reset circuit of STM32F4xx. OR	07 07
Q.3	(a) (b)	What are advantages of watchdog timer of 89C51rd2 microcontroller? Explain the watchdog timer in details. Compare Harvard and Van Neumann architecture.	07 07
Q.4	(a) (b)	Discuss the pulse width modulator mode of 89C51rd2 microcontroller with C-programming code. Explain high speed output toggle mode of 8051 microcontroller.	07 07 07
	(D)	OR	U7
Q.4	(a) (b)	Explain thumb-2 instruction set of ARM CORTEX. Explain Nested Vectored Interrupt Controller of ARM.	07 07
Q.5	(a)	Explain the round robin with interrupt architecture.	07
	(b)	Explain GPIO registers in detail.	07
0.5	(a)	OR Evaloin TIM 6 % TIM 7 hasia timora	07
Q.5	(a) (b)	Explain TIM 6 & TIM 7 basic timers. Explain features of the GPIO in detail.	07 07
