Q.3

## **GUJARAT TECHNOLOGICAL UNIVERSITY**

**BE - SEMESTER-VII (NEW) EXAMINATION - WINTER 2018** 

Subject Code: 2170906 Date: 15/11		2018	
Subject Name: Advanced Power Electronics			
Time: 1	Time: 10:30 AM TO 01:00 PM Instructions:  Total Marks: 7		
1. 2.	Attempt all questions.  Make suitable assumptions wherever necessary.  Figures to the right indicate full marks.		
Q.1 (a)	parameters.	03	
<b>(b)</b>	Write the advantages of resonance converter as compared to PWM converter.	04	
(c)	Illustrate the push pull converter with neat circuit diagram & waveform and derive the equation of output voltage in terms of input voltage & duty cycle.	07	
Q.2 (a)	Define duty cycle and write its importance.	03	
(b)	• •	04	
(c)	Justify the name of converter as zero voltage switching converter with necessary diagram and waveform.	07	
	OR		
(c)	Illustrate how the harmonic current are canceled by phase shifting transformer in 12 pulse rectifier.	07	
Q.3 (a) (b)	1 0	03 04	
(c)	1	07	

(b) Draw circuit diagram of 9 level asymmetric cascaded H bridge multilevel **04** inverter and mention the switching states to generate 0 level.

(a) Introduce the multi pulse converter.

- (c) The class E resonance inverter operates at resonance and has Vs = 12 V,  $R = 07 10 \Omega$ , fs = 25 kHz and Q = 7. Determine optimum value of L, C, Ce and Le.
- Q.4 (a) Draw block diagram of HVDC transmission system. Mention equipment 03 required for HVDC system.
  - (b) Justify: ZVS is better than ZCS. 04
  - (c) Explain in detail about rectifier and inverter control characteristic of HVDC 07 converter.

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- Q.4 (a) State the need of reactive power compensation.
  - (b) Draw and explain bipolar HVDC power transmission system based on 12 04 pulse converters for each pole.
  - (c) Discuss the operation of thyristor switched capacitor. 07
- Q.5 (a) Compare SVC and STATCOM. 03

03

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	<b>(b)</b>	Classify carrier based PWM technique for multilevel inverter. Discuss any one in detail.	04
	<b>(c)</b>	Explain in brief about FACTS.	07
		OR	
Q.5	(a)	Explain star/delta phase shifting transformer with phasor diagram.	03
	<b>(b)</b>	Write the advantages and limitations of SSSC.	04
	<b>(c)</b>	Explain how multilevel inverter is used as reactive power compensator with	07
		necessary vector diagram.	

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