Q.5

(a)

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GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-VII (OLD) - EXAMINATION - SUMMER 2017 Subject Code: 170701 Date: 09/05/2017 **Subject Name: Compiler Design** Time: 02:30 PM to 05:00 PM **Total Marks: 70 Instructions:** 1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. 0.1 (a) Explain various phases of compiler with example. 07 Explain input buffering methods. 07 Explain symbol table with two data structures suitable for it. **Q.2** 07 (a) Construct a DFA without constructing NFA for the following regular 07 **(b)** expression. (a | b) * aOR Explain subset construction method with an example. **(b) 07** Explain SLR parsing method with an example. Q.3 07 (a) How do you check whether the grammar is LL (1) or not? Justify your answer 07 **(b)** with appropriate example. OR **Q.3** Explain operator precedence parsing method. **07** (a) Check the following grammar is LR(1) or not. **(b)** 07 $S \rightarrow AaAb$ $S \rightarrow BbBa$ $A \rightarrow \varepsilon$ $B \rightarrow \epsilon$ Discuss differences between inherited attributes and synthesized attributes. 0.4 (a) 07 Write a short note on various representations of three address code. 07 **(b)** Explain syntax directed translation scheme with example. 0.4 (a) 07 **(b)** Define: DAG. Explain DAG representation of basic block with example. **07** Discuss various code optimization methods. Q.5(a) 07 **(b)** Explain code generator design issues. 07 OR

Explain static storage allocation technique.

Explain activation record organization in brief.

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