GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-VII(NEW) • EXAMINATION - WINTER 2016

Subject Code:2170914 Date:18/11/2016

Subject Name: Digital Signal Processing (Departmental Elective - II)

Time:10.30 AM to 1.00 PM Total Marks: 70

Instructions:

- 1. Attempt all questions.
- 2. Make suitable assumptions wherever necessary.
- 3. Figures to the right indicate full marks.
- Define sampling. State and explain sampling theorem. **07 Q.1** For the system described by y(t) = x(2t), determine whether the system is 07 (i) Stable (ii) causal (iii) linear (iv) time – invariant and (v) memory less or not. State and prove the relationship between z-transform and discrete time Fourier **Q.2** (a) 07 transform. Define 1) Signal 2) System. Classify them. 07 **(b)** State and prove properties of Fourier transform. **07** What is the importance of ROC in z transform? State the properties of z **Q.3** 07 transform and ROC. Explain the concept of pipelining in DSP. Also discuss the need of interlocking 07 in brief. OR 07 **Q.3** State and prove Parseval's relation for DTFT. (a) Describe any one type of DSP architecture. 07 **(b)** (a) Define the following terms: **07 Q.4** 1) State space 2) Correlation 3) ROC 4) Sampling 5) Aliasing 6) Impulse Response 7) Convolution. (b) Define periodicity. Check whether the following signals are periodic or not. If 07 the signal is periodic, then specify its fundamental period. (1) x1(t) = jej10t (2) x2(t) = e(-1+j)t (3) $x3(n) = ej7\pi n$. Explain Radix-2 FFT and DIT algorithm. **Q.4** 07 (a) Explain the structures for realization of FIR systems. 07 **(b)** Write short note on: Hilbert Transform. 07 Q.5 (a) Explain the structures for realization of IIR systems. **07 (b) Q.5** What are the different formats of fixed point representation? Explain the fixed **07** (a) point representation of binary numbers. Discuss the concept of zero input limit cycle oscillation. How this can be 07 eliminated?
