Seat No.: _____

Enrolment No.____

GUJARAT TECHNOLOGICAL UNIVERSITY

MCA - SEMESTER-I • EXAMINATION - SUMMER • 2015

	-	Code: 610004 Date: 12-05-2015	
Tin	-	Name: Fundamentals of Computer Organization 0:30 am - 01:00 pm Total Marks: 70	
Inst	1. 2. 3.	Attempt all questions. Make suitable assumptions wherever necessary.	
Q.1	(a)	 Answer following: Convert Binary 11101 to its equivalent grey code and hexadecimal. Draw K map for Y = ∑m (1, 5, 7, 9, 11, 13, 15) and simplify the expression. Also implement the simplified expression using appropriate gates. 	02 05
	(b)	 Answer following: Prove using method of perfect induction: (A + B)(A + C) = A + BC Draw K map for Y = ∑m (1, 3, 4, 5, 7, 9, 11, 13, 15) and simplify the expression. Also implement the simplified expression using appropriate gates. 	02 05
Q.2	(a)	Explain the working of instructions of 8086: MOV, ADD, MUL, CMP, DEC, XOR, JMP.	07
	(b)	What are various modes of data transfer? Explain DMA in detail. OR	07
	(b)	Describe different types of buses. Explain interface of buses with processor, memory and I/O devices.	07
Q.3	(a) (b)	Write notes on Dynamic Random-Access Memories with necessary diagrams. What do you mean by Addressing Techniques? Explain Indirect and Indexed Addressing techniques with an example. OR	07 07
Q.3	(a) (b)	Write short note on random-access memories. Describe two-address and zero-address instruction word formats.	07 07
Q.4	(a) (b)	Write short note on magnetic disk. What is a flip-flop? Explain characteristics of flip-flop. Explain RS flip-flop with waveform. OR	07 07
Q.4	(a)	Write short note on printer.	07
	(b)	Write a short note on Multiplexer with its working and applications.	07
Q.5	(a) (b)	Explain different addressing modes of 8086 with example. Draw the block diagram of 8086 and explain queue and segment registers. OR	07 07
Q.5	(a) (b)	Draw the block diagram of 8086 and explain Bus Interface Unit. Explain Execution Unit of 8086.	07 07
