Seat No.:	Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY

		MCA - SEMESTER- III EXAMINATION – WINTER 2018	
	U	Code: 630005 Date: 09-01-20	19
Su	bject	Name: System Software	
		0.30 am to 1.00 pm Total Marks: 7	0
Ins	tructio		
	2.	Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.	
Q.1	(a)	Do as directed 1. Give the advantages of PL Domain. 2. What is a language translator? 3. For allocation data structure, which of the following holds true? a. is created only once b. c. both 1 and 2 d. Only a. 4 allows allocation and deallocation of entities in a random order. a. Stack b. Trees c. Heaps d. Both b and c. 5. Analysis which determines the meaning of a statement once its grammatical structure becomes known is termed as analysis. a. Semantic b. Syntax c. Regular d. General 6. A parser which is a variant of top-down parsing without backtracking is a. Recursive Descend. c. Operator Precedence. b. LL(1) parser. d. LALR Parser. 7. Define: Absolute Loader	07
	(b)	Explain classification of Grammar.	07
Q.2	(a)	Explain the difference between Derivation and Reduction by taking suitable example.	07
	(b)	Which techniques are used for collision handling? Explain any one in brief.	07
	(b)	$\label{eq:order} \textbf{OR}$ Given the expression SI =P*R*N/100, identify the tasks that will be performed during lexical, sytax and semantic analysis.	07
Q.3	(a)	What do you understand by operator precedence parsing? Parse the following string giving the Diagrammatic trace of the algorithm. <id>a + <id>b * <id>c.</id></id></id>	07
	(b)	What is code optimization technique. Discuss techniques used by compilers for code optimization.	07
Q.3	(a)	What do you understand by top down parsing? Explain the prediction making mechanism. Give the Trace of top down parsing for source string $\mathbf{x} - 2 * \mathbf{y}$ to be parsed according to following grammar $E ::= E + T \mid E - T \mid T$ $T ::= T * F \mid T \mid F \mid F$ $F ::= < \text{digit} > \mid < \text{id} >$	07
	(b)	Explain the use of value numbers to determine the equivalence of occurrence of an expression in a block.	07
Q.4	(a)	Explain data structures used for Macro Processing.	07

	(b)	Construct all data structures for Macro given below	07
		MACRO	
		MCA &X, &Y, ® = BREG	
		AIF (&Y EQ 0) .ERR	
		MOVER & REG, & X	
		DIV ®, &Y	
		ERR MEND	
		Also generate the statements for Macro call $MCA 5$, 5 , $REG = AREG$	
		OR	
Q.4	(a)	Explain the procedure for expansion of Nested Macro calls in detail.	07
	(b)	Write a short note on program relocation with suitable example.	07
Q.5	(a)	Write an algorithm for Pass 1 of Assembler.	07
~	(b)	Write a short note on block device drivers.	07
	()	OR	
Q.5 (a	(a)	Explain Intermediate code forms for Imperative Statements generated by Assembler.	07
	()	Compare and contrast them.	
	(b)	Write a short note on character device drivers.	07
