Seat No.: \_\_\_\_\_ Enrolment No.\_\_\_\_

## **GUJARAT TECHNOLOGICAL UNIVERSITY**

MCA - SEMESTER-V • EXAMINATION - WINTER • 2014

|     | Sub        | oject Code: 2650005 Date: 05-12-2014   |            |
|-----|------------|--|------------|
|     | Sub        | ject Name: Parallel Programming  |            |
|     | Tin        | ne: 10:30 am - 01:00 pm Total Marks: 70  |            |
|     | Instr      | ructions:  |            |
|     |            | 1. Attempt all questions.  |            |
|     |            | <ol> <li>Make suitable assumptions wherever necessary.</li> <li>Figures to the right indicate full marks.</li> </ol> |            |
|     |            | 3. Figures to the right indicate full marks.   |            |
| Q.1 | (a)        | i. Define "Parallel Processing"  | 01         |
|     |            | ii. Define "Concurrency"   | 01         |
|     |            | iii. What is "cache coherence" issue occur in multiprocessors system?  | 02         |
|     |            | iv. What is "Induction Variable"?  | 02         |
|     | <b>(b)</b> | <ul><li>v. Define MPI.</li><li>i. How can semaphore be initialized and deleted?</li></ul>                            | 01<br>02   |
|     | (0)        | ii. Explain parameters of pthread_create() function.   | 02         |
|     |            | iii. Explain functionality of MPI_Init() function.   | 03         |
| Q.2 | (a)        | i. Explain Flynn's classification of computer architecture.  | 03         |
|     | (a)        | ii. What are the sources of performance loss? Explain any one in detail.   | 03         |
|     | <b>(b)</b> | List down six Parallel Computers. Explain any one in detail.   | 07         |
|     |            | OR   |            |
|     | <b>(b)</b> | i. Justify: "Multi-Core Computers, as an Opportunity"  | 03         |
|     |            | ii. Explain Random access machine (RAM).   | 04         |
| Q.3 | (a)        | i. Explain different phases of Generic Compilation Process with diagram.   | 05         |
|     |            | ii. What assumptions are made at the time of development of algorithm for P-   | 0.2        |
|     | <b>(b)</b> | RAM? What is array processor? Explain SIMD Computer Organization   | 02<br>07   |
|     | <b>(b)</b> | What is array processor? Explain SIMD Computer Organization.  OR   | U/         |
| Q.3 | (a)        | i. Explain forward dependency using block scheduling. Give an example of   | 04         |
|     | . ,        | forward dependency.  |            |
|     |            | ii. What is loop splitting? Explain with code snippet.   | 03         |
|     | <b>(b)</b> | Explain following process synchronization primitives.  | <b>07</b>  |
|     |            | <ul><li>i. lock_init(lockid)</li><li>ii. lock(lockid)</li></ul>  |            |
|     |            | iii. unlock(lockid)  |            |
| 0.4 | ( )        |  | ^ <b>-</b> |
| Q.4 | (a)        | What is the need of barrier? Explain barriers mechanism with code snippet.   | 07         |
|     | <b>(b)</b> | i. How can we create shared memory or delete shared memory in multi processing application using code?               | 05         |
|     |            | ii. Define fork() function using code.   | 02         |
|     |            | OR   | ~ <b>_</b> |
| Q.4 | (a)        | Explain following terminology of PVM   | <b>07</b>  |
|     |            | 1. Host 5. pvmd  |            |
|     |            | 2. Virtual Machine 6. Message  |            |
|     |            | <ul><li>3. Task</li><li>4. TID</li><li>7. Group</li></ul>  |            |
|     | <b>(b)</b> | Explain the need of mutual exclusion using POSIX thread.   | 07         |
|     | ()         | 1  |            |

| (a)        | What is the need of shared memory in parallel programming explain with code                               | 07  |
|------------|---|---|
| <b>(b)</b> | Explain following dependences.  | 07  |
| ` ′        | i. Anti dependence  |   |
|            | ii. Output dependence   |   |
|            | iii. Input dependence   |   |
|            | OR  |   |
| (a)        | Explain architecture of PVM with diagram.   | 07  |
| <b>(b)</b> | Explain collective communication in MPI with help of MPI_Scan(), MPI_Bcast() and MPI_Barrier() functions. | 07  |
|            | (b) (a)   | snippet.  (b) Explain following dependences.  i. Anti dependence  ii. Output dependence  iii. Input dependence  OR  (a) Explain architecture of PVM with diagram.  (b) Explain collective communication in MPI with help of MPI_Scan(), MPI_Bcast() |

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