Seat No.: \_\_\_\_\_ Enrolment No.\_\_\_\_

## **GUJARAT TECHNOLOGICAL UNIVERSITY**

MCA - SEMESTER-V • EXAMINATION - SUMMER • 2015

	•	Code: 650012 Date: 11-05-2015	
Su	bject	Name: Software Development for Embedded Systems	
Ti	me: 0	2:30 pm to 05:00 pm Total Marks: 70	
Ins	tructio	ns:	
	1.	Attempt all questions.	
	2.	ı v	
	3.	Figures to the right indicate full marks.	
0.1	( )		0.7
Q.1	(a)	Describe following terms:	07
		1. Technology	
		2. Embedded System	
		3. Renaissance Engineer	
		<ul><li>4. General Purpose Processor</li><li>5. Give Full Form: PSRAM, ISA</li></ul>	
		6. HM6264 and 27C256	
		7. Zero-bias Adjust	
	(b)	Describe following Design Metrics:	07
	(0)	NRE Cost	U /
		2. Time-to-market	
		3. Time-to-prototype	
		4. Correctness	
		5. Maintainability	
		6. Flexibility	
		7. Safety	
0.3	(.)	•	07
<b>Q.2</b>	(a)	Explain Single-Purpose Processors and Application Specific Processors.	07
	<b>(b)</b>	Write note on optimizing custom Single-Purpose Processors.  OR	07
	<b>(b)</b>		07
	<b>(b)</b>	Design a coffee machine controller, given that a coffee costs 75 cents and your machine accepts quarters only. Draw a black-box view, come up with a state	U/
		diagram and state table, minimize the logic, and then draw the final circuit.	
		diagram and state table, minimize the logic, and then draw the inial circuit.	
Q.3	(a)	Explain Datapath, Control Unit, and two memory architectures of General-	<b>07</b>
		Purpose Processor.	
	<b>(b)</b>	List Standard Single-Purpose Processors. Explain UART.	<b>07</b>
		OR	
<b>Q.3</b>	(a)	Describe direct, fully associative and set-associative Cache mapping techniques.	<b>07</b>
	<b>(b)</b>	Draw internal view and external block diagram for 8 x 4 RAM and ROM.	<b>07</b>
Q.4	(a)	Explain following:	
ν	(4)	1. Port and Bus-Based I/O	04
		2. Memory-Mapped I/O and Standard I/O	03
	(b)	Write note of priority arbiter, daisy-chain arbitration and network-oriented	07
	()	arbitration methods.	-
		OR	
<b>Q.4</b>	(a)	Explain following:	
-	` /	$1. I^2C$	03
		2. PCI Bus	02
		3. IrDA	02
	<b>(b)</b>	Draw functional block-diagram specification of a digital camera and explain it.	07
		Also draw block-diagram of the executable model of the digital camera.	

Q.5	(a)	Explain PROM Programmers, ROM Emulators and Flash for getting Embedded	07
		Software into the Target System.	
	<b>(b)</b>	Define Simulators? Discuss various useful abilities for Simulators.	<b>07</b>
		OR	
Q.5	(a)	Define Cross-Compiler, Cross-Assembler. Explain Linker/Locators for	<b>07</b>
		Embedded Software.	
	<b>(b)</b>	Discuss OVERFLOW.C, LEVELS.C and DBGMAIN.C modules for Tank	<b>07</b>
		Monitoring System.	

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